

# IP3416 Diagnostics

Two diagnostic RLL programs are supplied with the IP-EPS software. One will test all the digital inputs and outputs and the other will test the two high-speed inputs. The filenames of these programs are, *diag34.rll* and *diagHSI.rll* and listings are provided below.

To test the inputs and outputs of the IP3416, wire them up as shown in Figure 1. Then download the *diag34.rll* RLL program into the IP3416. This program will cycle the outputs on/off and sense the inputs for the proper state. If the outputs and inputs do not match, then the program will stop cycling through the outputs and display an error. The error number indicates which input failed and if it was a high (upper green LED ON) or low (lower green LED ON) failure.

To test the high-speed inputs, download the *diagHSI.rll* RLL program into the IP3416 and wire X32 and X33 as shown in Figure 2. Y14 and Y15 will provide 1KHz. PWM outputs. This pulse train will then be counted and if the value is less than 1 kHz an error condition will occur and the appropriate input number will be displayed to indicate an error.

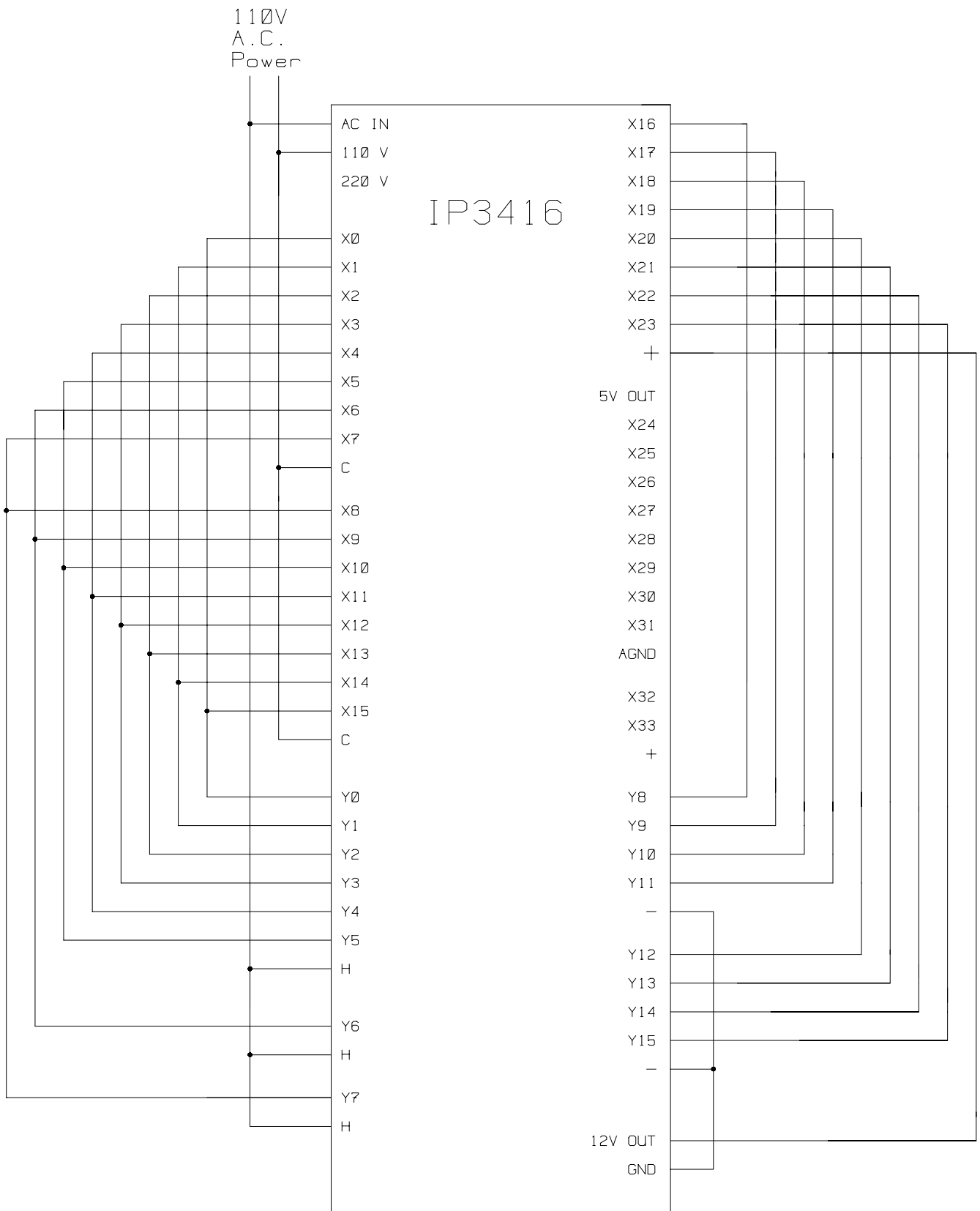
To test the analog inputs connect a 10 K $\Omega$  potentiometer to the specific input as show in Figure 3. Then place the following RLL into the PLC,

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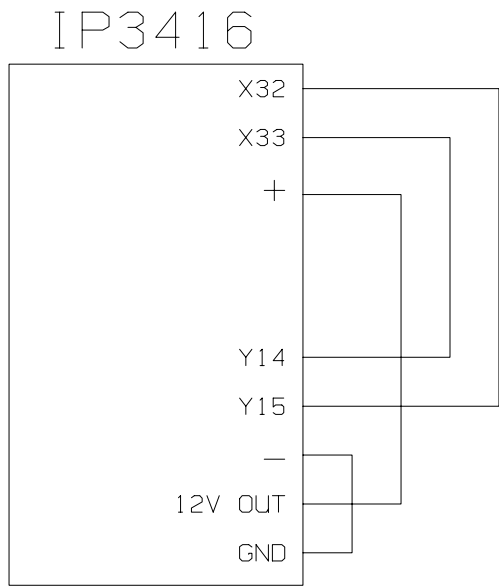
|
|
1 |-----[A24]-----| Display analog value of X24.
|
|
2 |----- (E) -----|
```

The program will display the contents of the analog value. Vary the potentiometer and see the specific change in the analog input. Change rung 1 for the specific analog input you are testing.

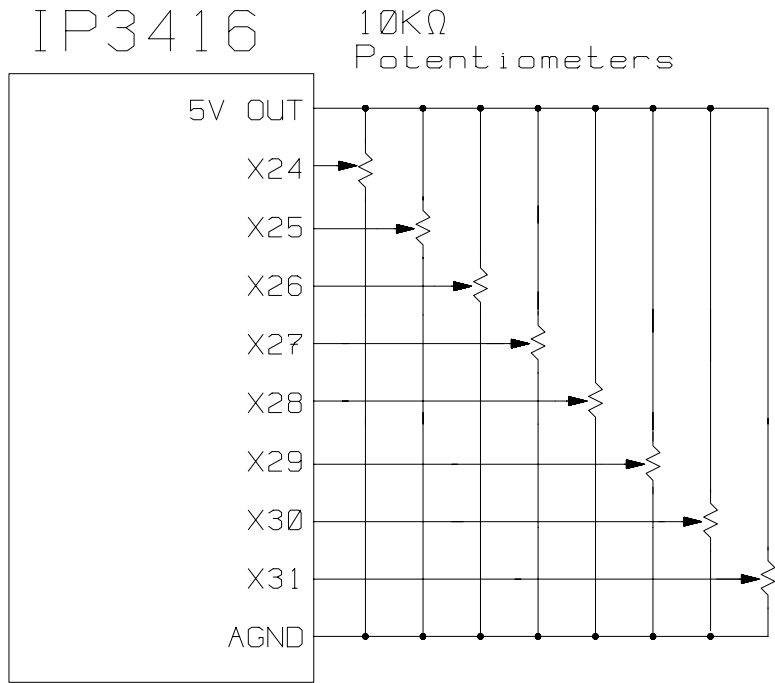
The PLCs shipped from the factory contain the self diagnostic program that turns outputs on and off randomly, it is important that you download your own application program to the PLC before you wire it to any live equipment in the field.



**Figure 1.** Wiring diagram for diagnostics



**Figure 2.** Wiring for high speed input diagnostics



**Figure 3.** Wiring for analog input diagnostics

# diag34

## IP3416 RELAY LADDER LOGIC

Line	Logic	Description
		IP3416 Diagnostics Program
1	T10 T1 1 - / ----- (7)-----	=====
2	T1 T10 2 -   ----- (3)-----	Generate clock T1 with .05 sec. off & .05 on.
3	T1 T2 3 -   ----- (2)-----	T2 is end .01 second portion of clock.
4	T1 R91 R101 4 - / --- ^ ----- ( )-----	R101 ticks every .1 second
5	R101 R40 5 -   ----- (SU2)-----	R40..R55 is shifted one bit every .1 second
6	R48 R40 6 - / ----- ( )-----	Make 8-bit Johnson counter for 2 second display cycle.
7	R40 R45 7 -   --- / ----- [C2-1]----	In first .5 sec. of cycle, display test count MS half.
8	R42 R45 8 -   ---   ----- [10000-C1]	In second .5 sec. of cycle, display test count LS half.
9	R42 R47 9 - / ---   ----- [C16]-----	In third .5 second of cycle display error code C16.
10	R40 R47 10 - / --- / ----- [ ]-----	In last .5 second of cycle, blank display
11	C16 11 - / ----- (J)-----	
12	R101 R0 12 -   ----- (SU3)-----	Make another shift register R0..R23 on .1 sec. ticks
13	R8 R0 13 - / ----- ( )-----	R0..R7 is an 8-bit Johnson counter.
14	R0 R90 C1 14 -   --- ^ ----- ( )-----	C1 is test loop counter.
15	C1 C1 15 -   ----- [10000]----	(C2,C1) counts (1,10000), (1,9999),..., (1,1), (2,10000),..., (2,1),
16	C2 16    ----- (^)-----	... so that (C2-1,10000-C1) is a 9-digit counter.
17	R0 Y0 17 -   ----- ( )-----	Map R0 to Y0;
18	R1 Y1 18 -   ----- ( )-----	R1 to Y1;
19	R2 Y2 19 -   ----- ( )-----	....



41		R24	Y12	X20	C16	
		-	---	---	/	[10020]---
42		R24	Y12	X20	C16	
		-	---	/ ---		[20020]---
43		R24	Y11	X19	C16	
		-	---	---	/	[10019]---
44		R24	Y11	X19	C16	
		-	---	/ ---		[20019]---
45		R24	Y10	X18	C16	
		-	---	---	/	[10018]---
46		R24	Y10	X18	C16	
		-	---	/ ---		[20018]---
47		R24	Y9	X17	C16	
		-	---	---	/	[10017]---
48		R24	Y9	X17	C16	
		-	---	/ ---		[20017]---
49		R24	Y8	X16	C16	
		-	---	---	/	[10016]---
50		R24	Y8	X16	C16	
		-	---	/ ---		[20016]---
51		R24	Y0	X15	C16	
		-	---	---	/	[10015]---
52		R24	Y0	X15	C16	
		-	---	/ ---		[20015]---
53		R24	Y1	X14	C16	
		-	---	---	/	[10014]---
54		R24	Y1	X14	C16	
		-	---	/ ---		[20014]---
55		R24	Y2	X13	C16	
		-	---	---	/	[10013]---
56		R24	Y2	X13	C16	
		-	---	/ ---		[20013]---
57		R24	Y3	X12	C16	
		-	---	---	/	[10012]---
58		R24	Y3	X12	C16	
		-	---	/ ---		[20012]---
59		R24	Y4	X11	C16	
		-	---	---	/	[10011]---
60		R24	Y4	X11	C16	
		-	---	/ ---		[20011]---
61		R24	Y5	X10	C16	
		-	---	---	/	[10010]---



diaghsi.prn

IP3416 RELAY LADDER LOGIC

1	1st s   R248  -   ------(S26)-----	On the first scan, set the PWM  parameters for Y14 and Y15 to  output a 1 kHz. signal.
2	   T15  ------(100)-----	Start a 1 second timer to wait  for everything to initialize.
3	T15  - / ------(J)-----	If 1 second warm-up hasn't  happened, then skip program.
4	X32  -   ------(^ )-----	Count the pulses on X32.
5	T0  - / ------(51)-----	Start a 1 second periodic timer.
6	T1  -   ------(51)-----	
7	T1 R100  -   --- ^ ------(S13)-----	Every second calculate the  number of pulses on both HS  inputs.
8	C5 T1  -   --- + ------[C10]-----	If C5 is 0, then there is no  error, so display number of  passes.
9	R101 C10   ^- ^ ------[C10+1]---	Increment number of passes.
10	C5 T1  -   --- / ------[ ]-----	There was an error, so blink  display and display number of  input with error.
11	C5  - / ------[C5]-----	
12	------(E)-----	=====
	C5  - / ------(RT)-----	CHECK HS INPUTS SUBROUTINE
	------(RT)-----	=====
13	C5  - / ------(RT)-----	Error already occurred, so skip  subroutine.
14	------(C31   [C16-1000]	Check X32 for >= 1000 pulses.
15	Overf   R31 C5  -   ------[32]-----	< 1000 pulses so set C5 to 32  to indicate error on X32.
16	C16  ------[0]-----	
17	C1  ------[D49*256+D48]---	Calculate current value of HSC.
18	C31  ------[C1-C0]---	Calculate number of pulse into  HSC.
19	Overf   R31 C31  -   ------[32767-C0+C1]---	HSC has wrapped around so  perform calculation for pulses.
20	C31  ------[C31-1000]	Check X33 for >= 1000 pulses.



	Overf		
	R31	C5	< 1000 pulses so set C5 to
21	-	[C5*100+33]	indicate X33 has error.
		C0	Make current value of HSC into
22	-----	[C1]-----	previous value.
			Return from subroutine.
23	-----	(RT)-----	=====
		PWM S	PWM FORMAT SUBROUTINE
		D53	=====
24	-----	[5]-----	Set PWM scale to 5 * 4 micro Sec
		Y14 P	= 0.02 msec
		D42	
25	-----	[50]-----	Set Y14 period to 50 * 0.02 msec
		Y14 D	= 1 msec
		D46	
26	-----	[40]-----	Set Y14 duty to 40 * 0.02 msec.
		Y15 P	= 0.8 msec.
		D43	
27	-----	[50]-----	Set Y15 period to 1 msec.
		Y15 D	
		D47	
28	-----	[40]-----	Set Y15 duty to 0.8 msec.
		C0	Store current value of HSC into
29	-----	[D49*256+D48]--	C0.
30	-----	(RT)-----	Return from subroutine
31	-----		